

[Web](#) [Images](#) [Maps](#) [News](#) [Shopping](#) [Gmail](#) [more ▾](#)
[Sign in](#)

"hardware execution unit" filetype:pdf

Search

[Advanced Search](#)
[Preferences](#)

Web

Results 1 - 8 of 8 for "hardware execution unit" filetype:pdf. (0.43 seconds)

An Execution Framework for MARTE-Based Models

hardware execution unit. Building a UML virtual machine requires a strict and precise semantics for UML. There is some ongoing work that aims at ...

ieeexplore.ieee.org/iel5/4492860/4492861/04492895.pdf - [Similar pages](#)

by C Mridha - 2005 - [Related articles](#) - [All 2 versions](#)

[PDF] A System Level Approach in Designing Dual-Duplex Fault Tolerant ...

File Format: PDF/Adobe Acrobat

Hardware Execution Unit. (Coprocessor). Fault Tolerant. Software Execution Unit. Figure 2. ... For example, by implementing the hardware execution unit ...

[ftp://ftp.elet.polimi.it/users/Cristiana.Bolchini/Papers/OLTW2002.pdf](http://ftp.elet.polimi.it/users/Cristiana.Bolchini/Papers/OLTW2002.pdf) - [Similar pages](#)

by C Bolchini - Cited by 7 - [Related articles](#) - [All 8 versions](#)

[PDF] T S V L M U D E C A P M -

File Format: PDF/Adobe Acrobat - [View as HTML](#)

hardware execution unit and the instruction. execution model the compiler has as a target., what has also been termed an interpreter. ...

pt.withy.org/publications/VLM.pdf - [Similar pages](#)

[PDF] Minimum waste scheduling of dynamic variable-latency and non ...

File Format: PDF/Adobe Acrobat - [View as HTML](#)

hardware execution unit, based on ideas taken from dynamic data flow machines. ... as a DAG derive a hardware execution unit with ...

doc.utwente.nl/38173/1/Mansour_def.pdf - [Similar pages](#)

by O Mansour - Cited by 4 - [Related articles](#)

[PDF] Model driven engineering for embedded real-time systems

File Format: PDF/Adobe Acrobat - [View as HTML](#)

hardware execution unit). The other solution consists in defining a transformation from the model to an existing, executable platform and formalism. ...

www-ist.csa.fr/publica/ist-exi-doc/200600003116.pdf - [Similar pages](#)

by Y Tanguy - [Related articles](#)

[PDF] Eliminating Exception Constraints of Java Programs for IA-64

File Format: PDF/Adobe Acrobat

speculation, since each such copy would require an additional hardware execution unit.

For example, a move instruction between floating-point registers ...

www.jilp.org/vol5/v5paper7.pdf - [Similar pages](#)

by K Ishizaki - 2003 - Cited by 7 - [Related articles](#) - [All 11 versions](#)

[PDF] Superscalar Processors CS 740 Oct. 3, 1997

File Format: PDF/Adobe Acrobat - [View as HTML](#)

Assign to hardware execution unit. Execute. • Perform computation or memory operation.

–Store's are only buffered. Retire / Commit (= Writeback) ...

www.cs.cmu.edu/afs/cs/academic/class/15740-97/public/info/superscalar-slide.pdf -

[Similar pages](#)

[PDF] Inside the Itanium 2 Processor

File Format: PDF/Adobe Acrobat - [View as HTML](#)

include an IA-32 hardware execution unit. The speed of the new process technology, as well as aggressive circuit design techniques, were key to ...

www.dig64.org/about/itanium2_white_paper_public.pdf - [Similar pages](#)

In order to show you the most relevant results, we have omitted some entries very similar to the 8 already displayed.

If you like, you can repeat the search with the omitted results included.

"hardware execution unit" filetype:pdf

Search

[Search within results](#) | [Language Tools](#) | [Search Tips](#) | [Dissatisfied? Help us improve](#) | [Try Google Experimental](#)

[Google Home](#) - [Advertising Programs](#) - [Business Solutions](#) - [Privacy](#) - [About Google](#)